D.No. 6410/XM/RU   Dated: 24.08.2024

Reference this office Notice D.No.6384/RU dated 06.08.2024

ADMISSION TEST AND PRESENTATION-CUM-INTERVIEW SCHEDULE FOR ADMISSION TO PH.D PROGRAMME UNDER VISVESVARAYA PH.D SCHEME FOR ELECTRONICS AND IT PHASE-II-2024-2025

S.No	Faculty	Date of Test	Date of Presentation - Cum - Interview
1.	Engineering & Technology	01.09.2024	02.09.2024
2.	Science	01.09.2024	02.09.2024

1. The Ph.D. Admission Tests and Presentation-Cum-Interview will be held at **Aligarh** only.
2. The detailed schedule is given in **Annexure-I**.
3. Admit Cards (for eligible candidates) will only be downloadable from the Controller's website **www.amucontrollerexams.com** on **26.08.2024**

Controller of Examinations

Distribution:

1. Deans of Faculty of Science/Engineering and Technology
2. Proctor / Dean, Students' Welfare
3. Chairmen/Coordinator as stated above
5. P.S. to Controller of Examinations
6. All D.C. /A.C/ In-charge Computer Cell
7. MIC (PRO)
8. Assistant Registrar, Vice-Chancellor's Secretariat
9. Guard file.

Schedule of Ph.D Admission Test and Presentation-Cum-Interview

Under Visvesvaraya Ph.D Scheme for Electronics and IT Phase-II-2024-2025

Particulars	Date	Timing	
Written Test*	01.09.2024	Section A 10:00 AM to 11:00 AM	Section B 11:15 AM to 12:15 PM
Presentation-Cum-Interview**	02.09.2024	08:30 AM onwards	

Note: - Candidates have to appear in both Section- A & Section -B of the Admission Test

* Venue of the Written Test will be mentioned on Admit Card

**Venue of Presentation Cum Interview:

Discipline	Venue
Computer Science	Department of Computer Science
Electronics Engineering	Department of Electronics Engineering
Computer Engineering	Department of Computer Engineering
Artificial Intelligence	Interdisciplinary Centre for Artificial Intelligence